

Alliance Memory Partners with Micron Semiconductor



Alliance Memory announced it has partnered with Micron Semiconductor to supply and extend availability of support for three 512M synchronous DRAM devices that Micron discontinued with Micron PCN #30995

Alliance Memory will be offering Micron's 32M x 16 MT48LC32M16A2P-75: C (Commercial Temperature), 32M x 16 MT48LC32M16A2P-75 IT: C (Industrial Temperature), and 64M x 8 MT48LC64M8A2P-75: C. In Addition to the Micron part numbers Alliance Memory will also offer Alliance-marked 512M SDRAMs

manufactured by Micron which will be 100% identical to the Micron parts.

| Micron Semiconductor Part Number | Alliance Memory Part Number | Density | Temperature Rating |
|----------------------------------|-------------------------------|----------|------------------------------|
| MT48LC32M16A2P-75:C | AS4C32M16SM-7TCN | 32M x 16 | 0 °C to +70 °C, Commercial |
| MT48LC32M16A2P-75 IT:C | AS4C32M16SM-7TIN | 32M x 16 | -40 °C to +85 °C, Industrial |
| MT48LC64M8A2P-75:C | Only will offer Micron part # | 64M x 8 | 0 °C to +70 °C, Commercial |

Devices with the Alliance Memory part numbers are 100% equivalent to the corresponding Micron part number, and each device has been manufactured to the same wafer/assembly and test materials and process as the corresponding Micron part number, with only the top side marking being different. Micron will manufacture these devices.

The MT48LC32M16A2P-75:C (AS4C32M16SM-7TCN), MT48LC32M16A2P-75IT:C (AS4C32M16SM-7TIN), and MT48LC64M8A2P-75:C are optimized for medical, industrial, automotive and telecom applications requiring high memory bandwidth, and are particularly well-suited to high-performance PC applications. Offered in a 54-pin TSOP II package, the devices operate from a single +3.3V ($\pm 0.3V$) power supply, and are lead (Pb) and halogen free.

PC100- and PC133-compliant, the devices provide programmable read or write burst lengths of 1, 2, 4, 8, or full page, with a burst termination option. An auto pre-charge function provides a self-timed row pre-charge initiated at the end of the burst sequence. Easy-to-use refresh functions include auto- or self-refresh, while a programmable mode register allows the system to choose the most suitable modes to maximize performance.

Datasheets and additional information can be found on www.alliancememory.com