

New Low-Power 32M CMOS SRAM

Alliance Memory expanded its line of legacy low-power CMOS SRAMs with a new 32M IC (2M x 16 / 4M x 8 switchable). This is the company's highest density low-power device to date which operates from a single power supply of 2.7 to 3.6V and offers a fast access time of 55 ns. The AS6C3216 is optimized for low-power industrial, telecom, medical and automotive applications. It is particularly well-suited for battery backup non-volatile memory.



The device is offered in the 48-pin, 12mm x 20mm TSOP-I package. The AS6C3216 features low power consumption with a typical operating current of 45 mA and standby current of 10 μ A. All inputs and outputs are fully TTL-compatible.

The AS6C3216 is fabricated using very high-performance, high-reliability CMOS technology and its current, is stable within the -40°C to +85°C operating temperature range. The device offers fully static operation and tri-state output and also features a data retention voltage of 1.2V minimum.

Alliance Memory's legacy ICs provide reliable drop-in, pin-for-pin-compatible replacements for a number of similar solutions. The AS6C3216 is the latest in the company's full range of low-power SRAM products which include devices with densities of 64K, 256K, 1M, 2M, 4M, 8M, 16M and now 32M.

Datasheets and additional information can be found on www.alliancememory.com